

ABSTRACT

The present invention avoids the occurrence of hold violation in a scan diagnosis circuit. The hold violation is avoided by connecting the scan chains so as to flow scan
5 data in the direction opposite to the direction of propagation of clock signal to accelerate the transition of clock signal with respect to the scan test data, and by increasing the resistance in the return path more than that in the clock signal transmission path to delay the data
10 transfer in the return path.